

ABSTRACT OF THE DISCLOSURE

A liquid crystal display device for restraining a generation of transient current is disclosed. In the device, a line memory divides a data for at least one line inputted from the exterior thereof into a plurality of groups to store the divided data therein and outputs the data at a desired unit from each of the groups. A driving circuit includes n driver integrated circuits (wherein n is an integer) that are connected to the line memory and a liquid crystal display panel to drive the liquid crystal display panel in response to the data outputted from the line memory. A timing controller is connected to the line memory and the driving circuit to receive a data clock inputted from the exterior thereof for outputting the data from the plurality of groups of said line memory to the driving circuit every period of the data clock in response to a time corresponding to the number of said groups.